File No.: 14836-9US AD/mb

Montreal, Canada April 25, 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Applicant:

MICROBRIDGE TECHNOLOGIES INC.

Serial No.:

(unknown)

Filed:

(unknown)

Title:

METHOD FOR PRODUCING A PACKAGED INTEGRATED

CIRCUIT WITH A MICROCAVITY

Group Art Unit:

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Examiner:

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Agent of Record:

Alexandra Daoud, Reg. N° 55,992

MAIL STOP - DD

U.S. Patent and Trademark Office

P.O. Box 1450

Alexandria, Virginia 22313-1450

U.S.A.

## INFORMATION DISCLOSURE STATEMENT PRIOR TO FIRST OFFICE ACTION

Sir:

Pursuant to the duty of disclosure under 37 CFR 1.56, copies of the references listed on the attached PTO Form SB/08A/B are submitted herewith.

The references cited in an International Search Report (ISR) mailed on <u>July 16</u>, 2004 by the EPO in connection with a corresponding PCT application are also included. A copy of the ISR is also enclosed.

In accordance with 37 CFR 1.97(h), the submission of the present information is not to be construed as an admission that such information is, or is considered to be material to patentability.

12 Rec'd PCT/PTC 2 5 APR 2005

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## U.S. Patent and Trademark Office

The Examiner is kindly requested to consider these references during the examination of the above-identified application, making the references of record, and to return an initialed copy of the SB/08A/B Form to the below-signed agent.

Respectfully submitted,

MICROBRIDGE TECHNOLOGIES INC.

By:

Alexandra Daoud

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Montreal, Quebec, H3A 2Y3

Tel.: (514) 847-4244

Enc.: Copies of references and International Search Report

2 sets of SB/08A/B Form

PTO/SB/08A/8 08-03)

Substitute for form 1449PTO				Complete if Known		
04004101010101				Application Number -		
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STATEMENT BY APPLICANT				First Named Inventor	Leslie M. Landsberger et al.	
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Sheet	1	of	2	Attorney Docket Number	14836-9US	

	U.S. PATENT DOCUMENTS							
Examiner Cite Initials No.1		Document Number Number - Kind Code <sup>2</sup> (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear			
		US-6,379,988	April 30, 2002	Kenneth A. Peterson et al.				
		US-2001/055864	Dec. 27, 2001	Chan et al.				
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code3 - Number4 - Kind Code5 (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant Of Cited Document	Pages, Columns, Unes Where Relevant Passages or Relevant Figures Appear	۲'	
		WO02/18785	March 7, 2002	CANTOR Hal C. et al.			
		WO03/023794	March 20, 2003	Microbridge Technologies Inc.			
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Examiner	Date
Signature	Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. \*Applicant's unique citation designation number (optional). \*See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04\* \*See The Police that issued the document, by the two-letter code (WIPO Standard ST.3). \*For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. \*Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. \*Applicant is to place a check mark here if English language Translation is

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(use as many sheets as necessary)				Examiner Name	-	
Sheet	2	of	2	Attorney Docket Number	14836-9US	

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T²		
		ELDERSTIG H ET AL: "Spin Deposition on a Polymers Over Holes and Cavities", Sensors and Actuators A. Elsevier Sequola S.A., Lausanne, Switzerland, Vol. 46, No. 1-3, Jan. 1995, pp. 95-97.			
		GRUDIN et al., "Microstructure Release and Test Techniques for High-Temperature Micro Hotplate", Electrical and Computer Engineering, 1999, IEEE Canadian Conference on Edmonton, Alberta, CA, 9-12 May 1999, pp. 1610-1615.			
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<sup>1</sup> Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.